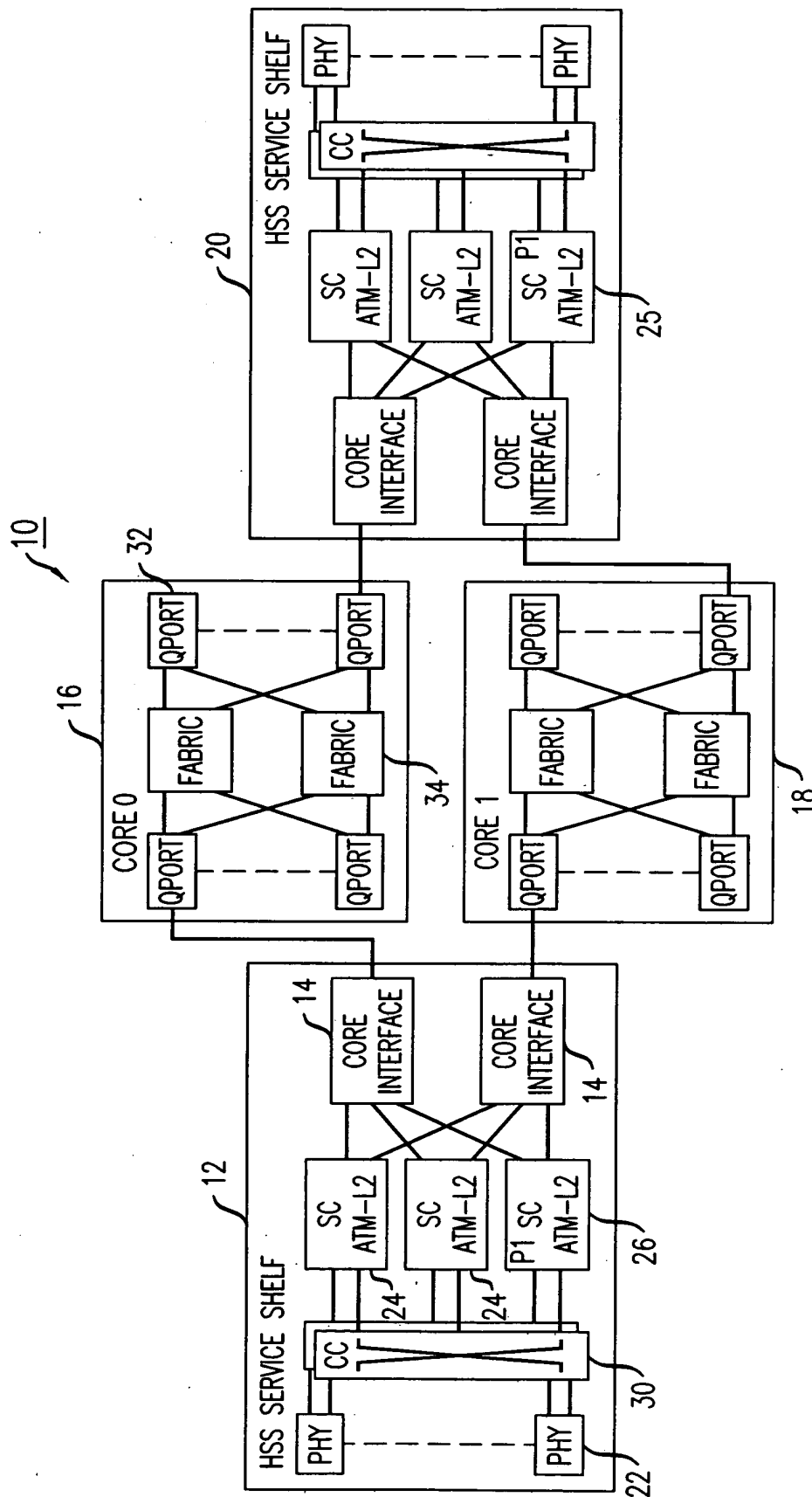


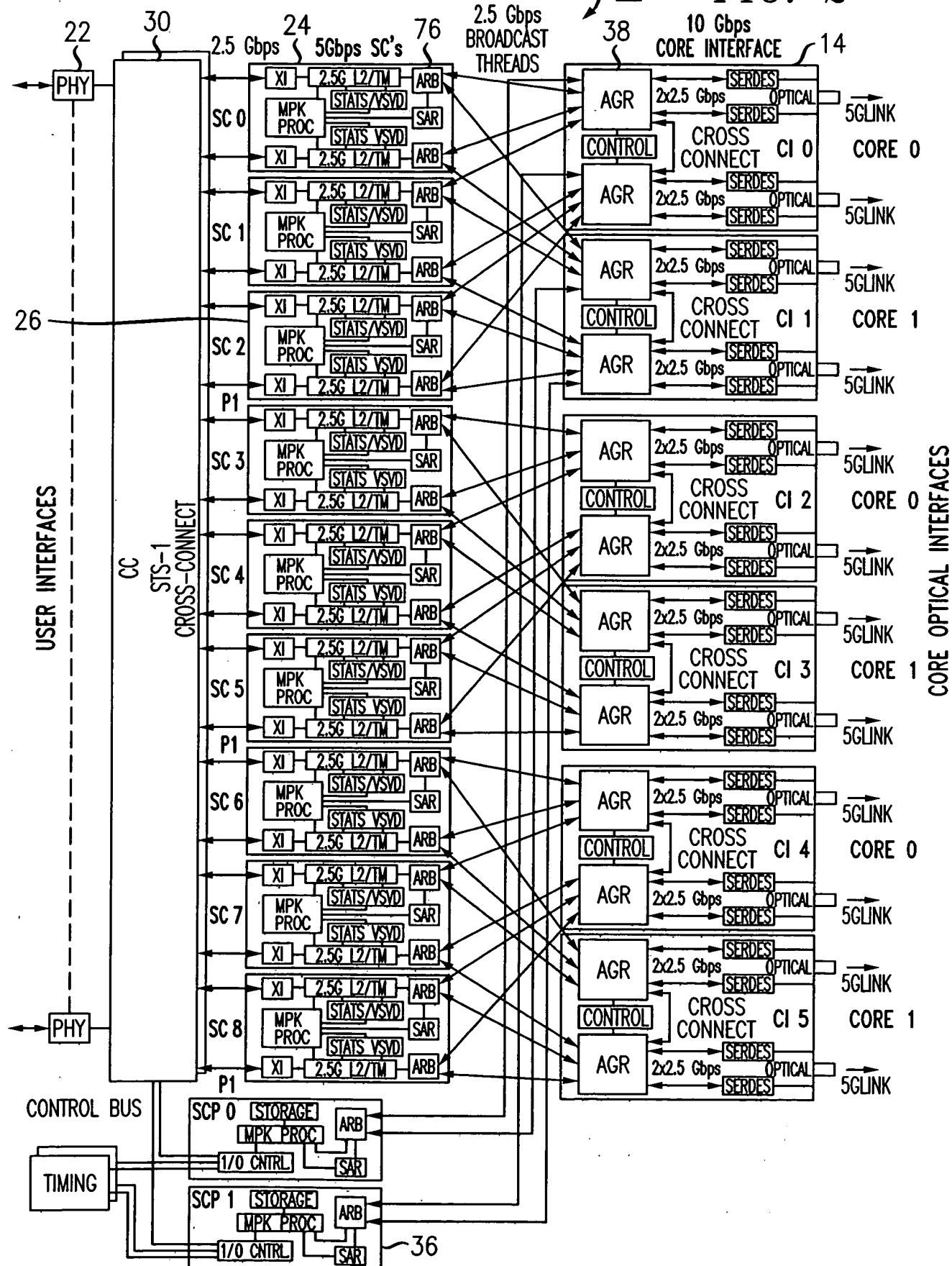


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FIG. 1



**10 Gbps  
CORE INTERFACE**



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FIG. 3

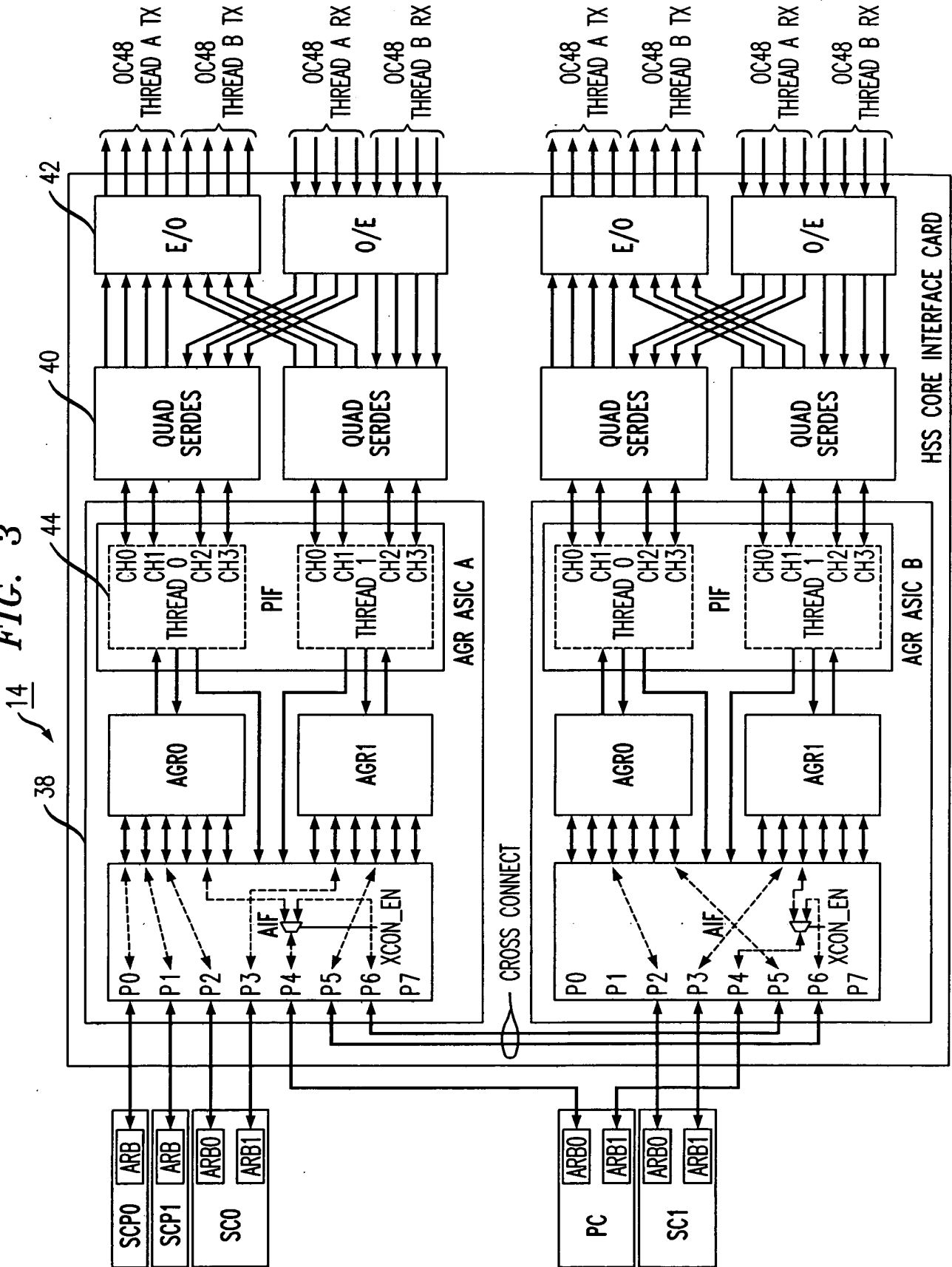
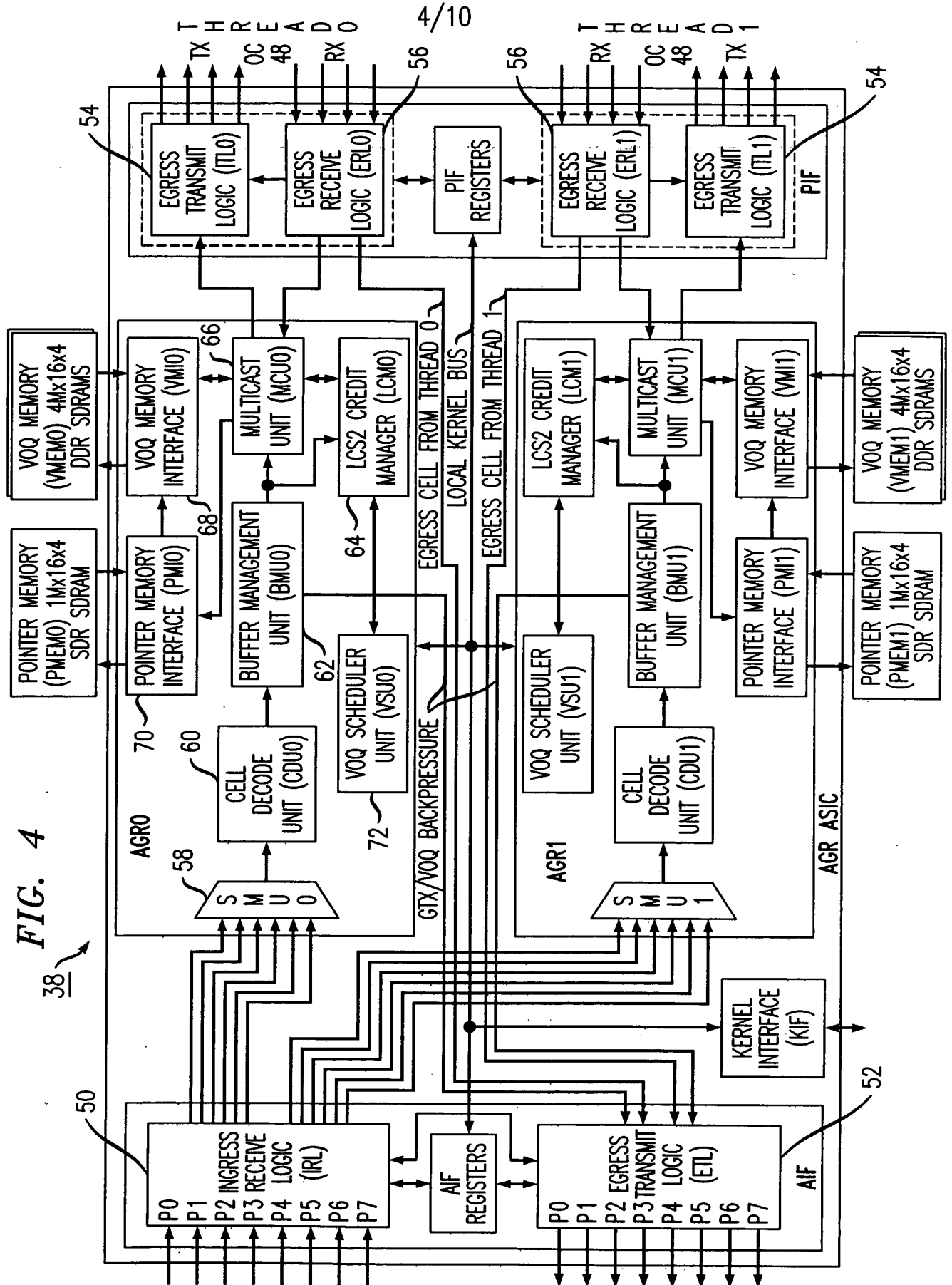
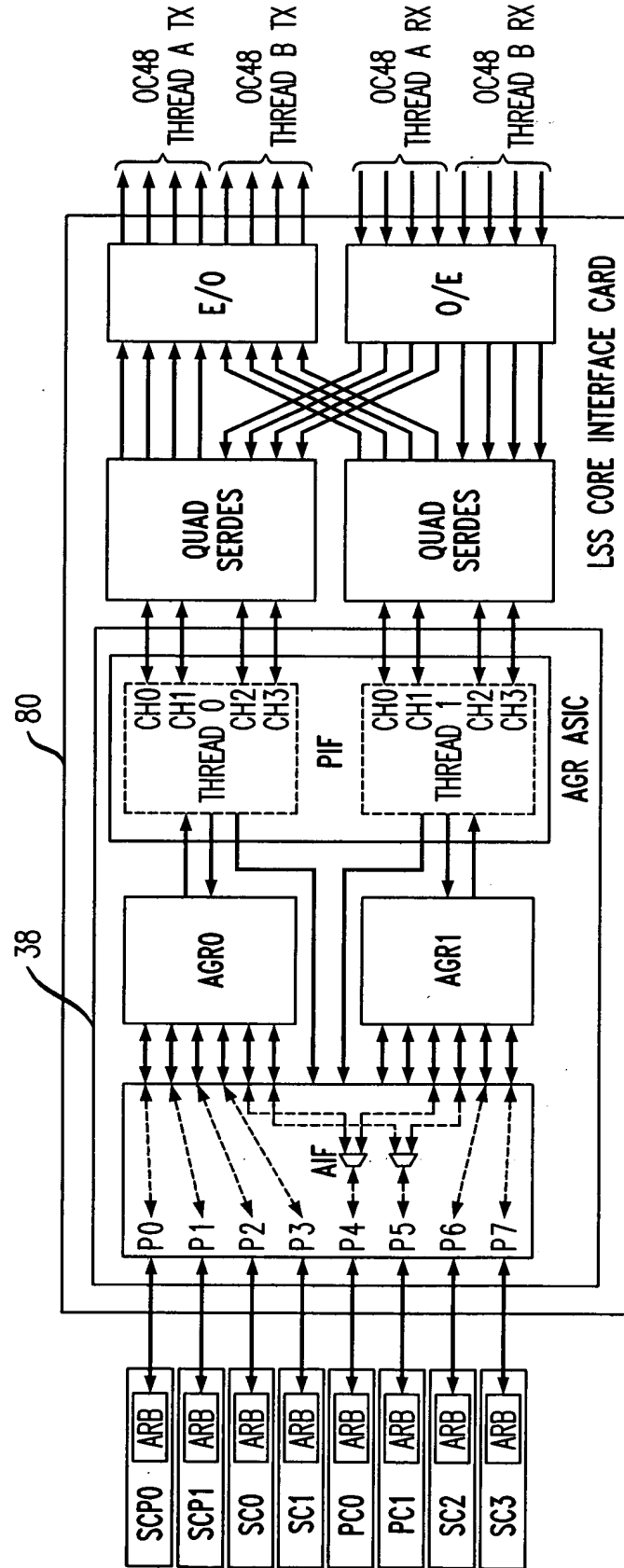


FIG. 4



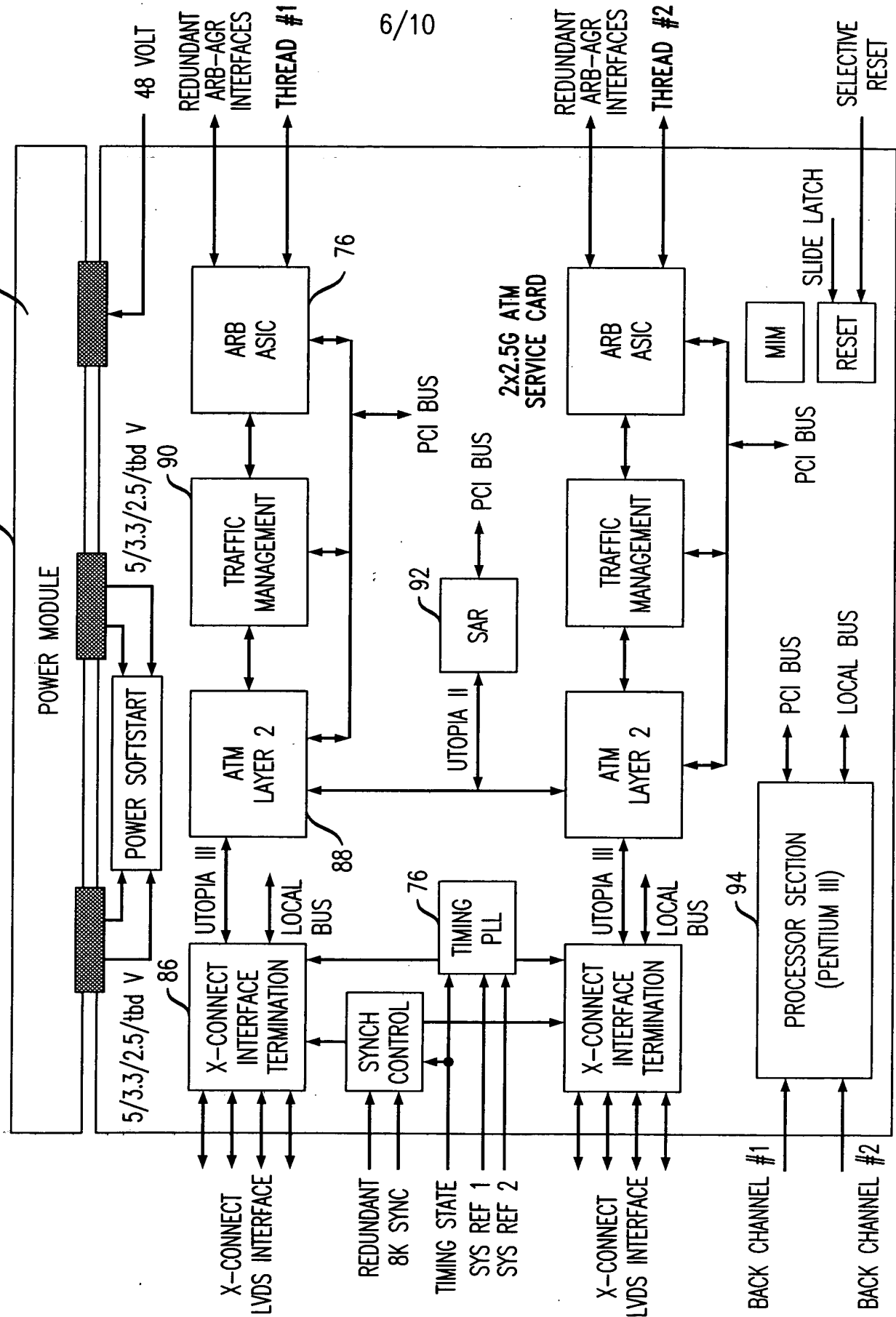
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FIG. 5

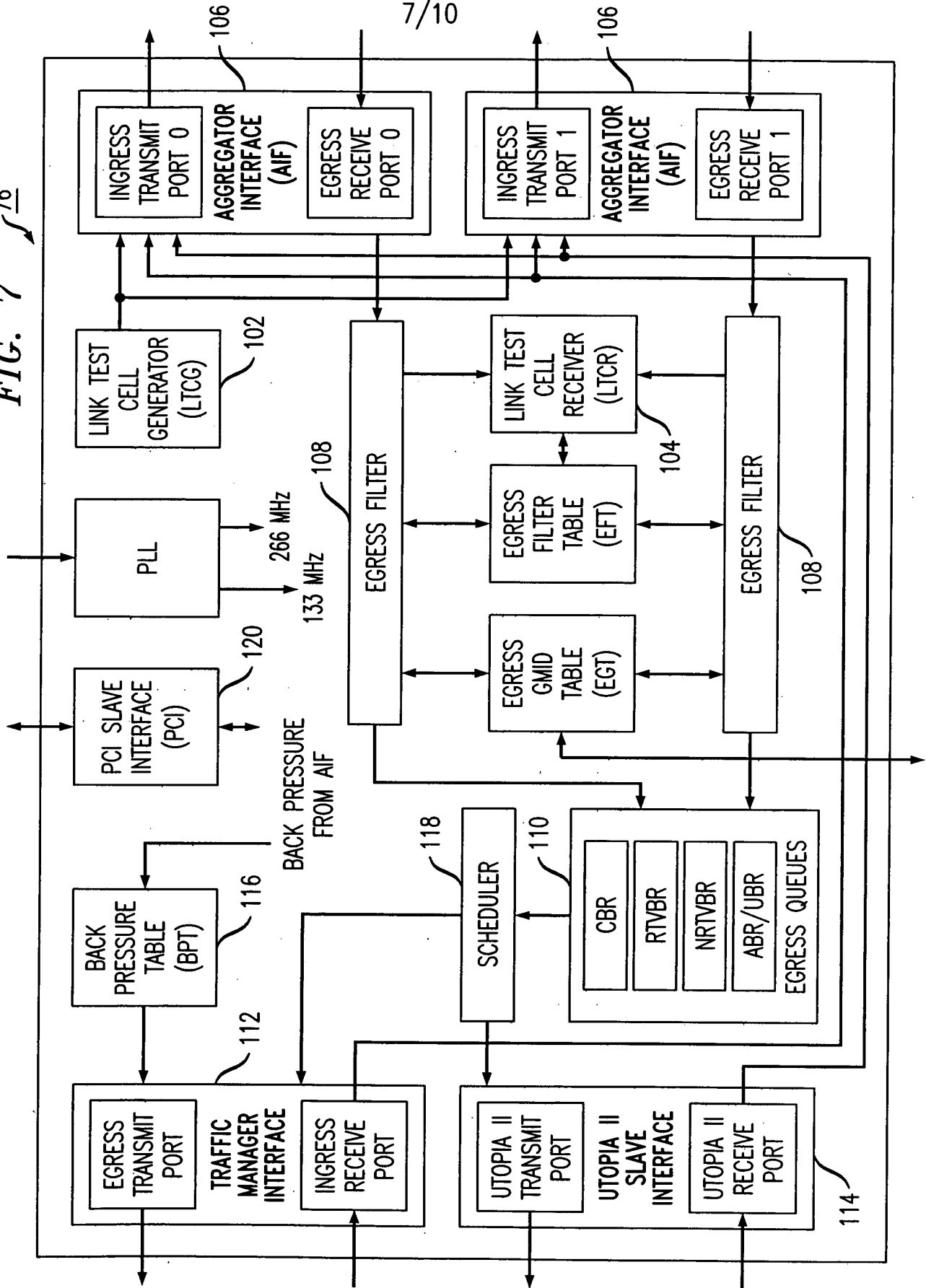


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FIG. 6



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LTC GENERATOR TABLE

|                             | ENABLE TX FOR FLOW |     |     |     |     |     |     |     |                                   |
|-----------------------------|--------------------|-----|-----|-----|-----|-----|-----|-----|-----------------------------------|
| x.y (10 bits)<br>INDEX (1K) | Z7                 | Z6  | Z5  | Z4  | Z3  | Z2  | Z1  | Z0  | POINTER TO NEXT<br>FLOW (10 bits) |
| 0                           | 1/0                | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 |                                   |
| ...                         | 1/0                | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 |                                   |
| 1048                        | 1/0                | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 | 1/0 |                                   |

LTC GENERATOR FIELD DEFINITIONS

| FIELD | DEFINITION   |
|-------|--|
| Z7-Z0 | ENABLE TEST CELL GENERATION FOR THE PARTICULAR Z FLOW.<br>1 = SEND LTC ON FLOW<br>0 = DO NOT SET LTC ON FLOW |
| PTR   | POINT TO NEXT LOCATION OR NEXT FLOW IN THIS TABLE THAT<br>CONTAINS TEST CELL INFORMATION TO SEND.            |

FIG. 9

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LTC RECEIVER TABLE

| x.y (10 bits)<br>INDEX (1K) | ENABLE<br>CHECK<br>[7:0] | ENABLE<br>CHECK<br>[7:0] | CELL<br>RCVD<br>[7:0] | CELL<br>RCVD<br>[7:0] | FAULT<br>[7:0] | FAULT<br>[7:0] | COUNT<br>[9:0] | COUNT<br>[9:0] | ERROR<br>CNT<br>[3:0] | ERROR<br>CNT<br>[3:0] |
|-----------------------------|--------------------------|--------------------------|-----------------------|-----------------------|----------------|----------------|----------------|----------------|-----------------------|-----------------------|
|                             | CORE 0                   | CORE 1                   | CORE 0                | CORE 1                | CORE 0         | CORE 1         | CORE 0         | CORE 1         | CORE 0                | CORE 1                |
| 0                           | XXXXXXXX                 | XXXXXXXX                 | XXXXXXXX              | XXXXXXXX              | XXXXXXXX       | XXXXXXXX       | XXXXXXXX       | XXXXXXXX       | XXXX                  | XXXX                  |
| ...                         | XXXXXXXX                 | XXXXXXXX                 | XXXXXXXX              | XXXXXXXX              | XXXXXXXX       | XXXXXXXX       | XXXXXXXX       | XXXXXXXX       | XXXX                  | XXXX                  |
| 1023                        | XXXXXXXX                 | XXXXXXXX                 | XXXXXXXX              | XXXXXXXX              | XXXXXXXX       | XXXXXXXX       | XXXXXXXX       | XXXXXXXX       | XXXX                  | XXXX                  |

LTC GENERATOR FIELD DEFINITIONS

| FIELD        | DEFINITION   |
|--------------|--|
| ENABLE CHECK | ENABLE TEST CELL VERIFICATION FOR THAT x.y.z.  |
| CELL RCVD    | SET WHEN A CELL COMES IN ON A FLOW. CLEARED BY TABLE CHECKER.  |
| FAULT        | SET WHEN A LINK IS DECLARED FAULTED, RESET WHEN LINK IS DECLARED GOOD.   |
| COUNT        | START COUNTING UP WHEN THE TABLE HAS BEEN SCANNED AND ENABLE IS SET FOR A FLOW BUT A LTC HAS NOT BEEN RECEIVED ON THAT FLOW. WHEN THE PROGRAMMABLE THRESHOLD THAT INDICATES WHEN TO FLAG A FLOW AS BAD IS REACHED, AND INTERRUPT WILL BE GENERATED TO THE PROCESSOR INTERFACE. ANY Z CAN BE 'BAD' FOR THE COUNT TO INCREMENT FOR THE x.y. WHEN THERE ARE NO ERRORS AFTER AN ERROR CONDITION, THIS WILL START TO COUNT THE NUMBER OF GOOD CHECK CYCLES FOR A FLOW. AFTER THE PROGRAMMABLE THRESHOLD THAT INDICATES WHEN A FLOW IS CONSIDERED GOOD IS REACHED THE FLOW IS DECLARED GOOD AND THE PROCESSOR IS NOTIFIED VIA AN INTERRUPT IF ENABLED. |
| ERROR CNT    | COUNT OF TEST CELLS WITH FIXED PAYLOAD ERRORS.   |

FIG. 10